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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet

1

of

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Complete if known

Application Number	
Filing Date	16 July 2003 (herewith)
First Named Inventor	Chih Yuan Huang et al.
Group Art Unit	
Examiner Name	
Attorney Docket Number	MXIC 1532-1

U.S. PATENT DOCUMENTS

Examiner Initials ¹	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
Ch	A1	4,963,506		Liaw et al.	10-16-1990	
	A2	5,751,637		Chen et al.	05-12-1998	
	A3	5,753,134		Biebl	05/19/1998	
	A4	5,874,129		Beinglass et al.	02-23-1999	
	A5	6,268,068		Heuer et al.	07-31-2001	
	A6	6,373,092		Okumoto	04-16-2002	
Ch	A7	10/180,168		Luoh	06-26-2002	(filing date)
	A8					
	A9					
	A10					
	A11					
	A12					
	A13					
	A14					
	A15					
	A16					
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FOREIGN PATENT DOCUMENTS

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		Office ³	Number ⁴				
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	B4						
	B5						
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Examiner Signature

Andy Nguyen

Date Considered

09/22/04

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²Unique citation designation number. ³See attached Kinds of U.S. Patent Documents. ⁴Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁵For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁶Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁷Applicant is to place a check mark here if English language Translation is attached.

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Sheet

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Group Art Unit	
Examiner Name	

Attorney Docket Number MXIC 1532-1

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS		
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
Ah	C1	KAHLER, U., et al. "Size related properties of silicon nanoparticles from decomposition of SiOx" http://www.mpi-halle.mpg.de/annual_reports/2000.pdf ; 2 pages.
	C2	"Polysilicon: The Successor Technology," http://www.wtec.org/loyola/dspla_jp/c5_s5.htm (June 1992), 3 pages
	C3	Polysilicon TFT, The Future is clear with Toshiba, http://www.toshiba-europe.com/computers/tnt/tft/ (2002), 1 page
	C4	Higher Resolution, The Future is clear with Toshiba, http://www.toshiba-europe.com/computers/tnt/tft/resolution.htm (2002), 1 page
	C5	Polysilicon Explained, The future is clear with Toshiba, http://www.toshiba-europe.com/computers/tnt/tft/explained.htm (2002), 1 page
	C6	"Flash Memory Technology" Chapter 10, Integrated Circuit Engineering Corporation, http://smithsonianchip.ssi.edu/ice/cd/MEMORY97/SEC10.PDF (1997), pp. 10-1 through 10-16.
	C7	WILSON, RON, "Flash gets a quantum makeover at IEDM," EETIMES (12/13/2002), 4 pages.
	C8	"The K-Input Floating-Gate MOS (FGMOS) Transistor" http://people.ece.cornell.edu/minch/talks/FGMOSDeviceLayout.pdf , 22 pages
	C9	DIPERT, BRIAN, "Exotic Memories, diverse approaches" www.ednmag.com (04/26/2001), 9 pages
	C10	LAMMERS, DAVID, "Chip makers bang away at metal gates" EETIMES (11/26/2002), 3 pages
Ah	C11	"Characterizing Very Thin ONO Gates with Laser Spectroscopic Ellipsometry" Rudolph Technologies, Inc. Application Reports (2001) http://www.rudolphtech.com/reports/VeryThinONOGates.htm , 3 pages

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<p>Sheet</p>		<p>3</p>	<p>of</p>	<p>3</p>	<p>Attorney Docket Number</p>
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				<p>Group Art Unit</p>	
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

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